

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Charan K. Gurumurthy et al.

Title: A METHOD OF FORMING AN INTEGRATED CIRCUIT SUBSTRATE

Docket No.: 884.872US1

Filed: June 30, 2003

Examiner: Lynne Gurley

Serial No.: 10/612282

Due Date: April 23, 2004

Group Art Unit: 2812



Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

We are transmitting herewith the following attached items (as indicated with an "X"):

A return postcard.  
 Response to Restriction Requirement (2 pgs.).

If not provided for in a separate paper filed herewith, Please consider this a PETITION FOR EXTENSION OF TIME for sufficient number of months to enter these papers and please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.  
Customer Number 21186

By: Ann M. McCrackin  
Atty: Ann M. McCrackin  
Reg. No. 42,858

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 23 day of April, 2004.

Anne M. Richards  
Name

Anne M. Richards  
Signature

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.  
(GENERAL)



SN 10/612282

PATENT

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Serial No.:	10/612282	Group Art Unit:	2812
Filed:	June 30, 2003	Docket:	884.872US1
Title:	A METHOD OF FORMING AN INTEGRATED CIRCUIT SUBSTRATE		
Assignee:	Intel Corporation	Customer No:	21186

**RESPONSE TO RESTRICTION REQUIREMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

In response to the Restriction Requirement mailed March 23, 2004, Applicant hereby elects, with traverse, Group I, claims 1-27. If the restriction is continued, Applicant hereby cancels the remaining claims 28-30 without prejudice or disclaimer, and reserves the right to reintroduce them in one or more divisional applications at a later date.

The Restriction Requirement is traversed on the basis that Restriction Requirements are optional in all cases. MPEP 803. If the search and examination of an entire application can be made without serious burden, the Examiner must examine it on the merits, even though it arguably may include claims to distinct or independent inventions. MPEP 803. All of the pending claims relate to methods of forming a substrate or integrated circuit assembly and computer systems that include substrates and integrated circuit assemblies formed by the method. Therefore, the Examiner will be looking for methods of forming substrates and integrated circuit assemblies as claimed in the present application generally as well as part of a computer system. Thus, Applicant respectfully submits that these claims can all be easily searched and examined together.

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Page 2

Dkt: 884.872US1

The Examiner is invited to telephone Applicant's attorney, Andrew Peret at (262) 646-7009, or ~~Applicant's~~ below-signed representative at (612) 349-9592 to facilitate the prosecution of this application. If necessary, please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

Respectfully submitted,

CHARAN K. GURUMURTHY ET AL.

By their Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.  
Attorneys for Intel Corporation  
P.O. Box 2938  
Minneapolis, Minnesota 55402  
(612) 349-9592

Date April 22, 2004

By

Ann M. McCrackin

Ann M McCrackin

Reg. No. 42,858

Name

Anne M. Richards

Signature

Anne M. Richards

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on this 23 day of April, 2004.